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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Katsuhiko Fukasaku

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE
SAME

Assistant Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits and calculation of the filing fee, please amend the
above-identified application as follows:

IN THE SPECIFICATION:

Please amend the paragraphs beginning at page 7, lines 3-16 as follows:

FIGS. 1(A)-1(D) are process diagrams for showing a method for manufacturing an
NMOSFET device according to a first embodiment of the present invention;

FIGS. 2(E)-2(H) are continued process diagrams for showing a method for
manufacturing the NMOSFET device according to the first embodiment of the present
invention;

FIGS. 3(I)-3(L) are further continued process diagrams for showing the method for
manufacturing the NMOSFET device according to the first embodiment of the present